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## **PALTEK Delivers Evaluation Board for Low-Cost PCI Express Solution**

Booking Starts July 20<sup>th</sup>, 2006  
Low Cost FPGA x4 PCI Express Solution with external PHY

PALTEK CORPORATION (JASDAQ: 7587), a leading silicon and electronic solutions supplier in Japan, announced today that PALTEK developed the evaluation board for low cost x4 PCI Express (one lane is 2.5Gbps) solution, and that the board will be available for booking starting July 20<sup>th</sup>, 2006.

This PCI Express evaluation board is fully x4 PCI-SIG compliant with Xilinx's cost competitive FPGA and Genesys Logic's external PHY. The board is designed with Xilinx's Spartan<sup>TM</sup>-3 generation FPGA and Genesys Logic's PCI Express GigaCourier<sup>TM</sup> GL9714. By developing this cost effective solution, PALTEK aims to meet customer's strong requests for low cost PCI Express solutions.

PCI Express is the latest high-speed interface for data transfer and is now being adapted to such broad applications like servers, computers, communication equipment, and digital consumer appliances as well as industrial equipment. As the needs for high-speed data transfer of large data increase, higher data transfer rate is demanded and PCI Express, which can support from 2.5Gbps up to 80Gbps, is regarded to succeed PCI. PALTEK PCI Express evaluation board, which delivers low-cost x4 PCI Express solutions, will be a truly valuable tool to designers' time-to-market needs.

The great achievement of PALTEK PCI Express evaluation board is that 100% x4 PCI Express compliance has been realized with Xilinx's cost-competitive Spartan<sup>TM</sup>-3 FPGA, combined with the industry's first x4 PCI Express 10Gbps PHY, GL9714, brought by Genesys Logic. Up till now, in order to realize multi-lane PCI Express solution, the designers often have to use high-end and high-price FPGAs.

PALTEK also announced the scheduled seminars for PCI Express solution, which designers can learn PCI Express solution from designing methods to device selection, to verification process. The seminars are scheduled on July 21 at Osaka and July 27<sup>th</sup> and 28<sup>th</sup> at Shin-Yokohama. Further information can be obtained at <http://www.paltek.co.jp/seminar/pcie/0607.htm>

Dedicated PCI Express Solution web site is also opened and seen at <http://www.paltek.co.jp/tech/pciex/palboard/index.htm>. The quotes for the evaluation board can also be obtained from this site.

The evaluation board will be available for booking from July 20<sup>th</sup>, 2006, and will be shipped within the 3<sup>rd</sup> quarter of 2006.

Mr. Osamu Sawada, Senior Product Marketing Manager at Xilinx Japan, said, "PALTEK PCI Express Evaluation Board delivers a true low-cost PCI Express solution by using the industry's price leader of Spartan<sup>TM</sup>-3 FPGA. Most of all, by realizing high-demand x4 PCI Express solution with Spartan-3 FPGA, it will significantly widen the potential applications and market coverage.

Mr. Linus Wang, Senior Sales Manager at Genesys Logic said, “Our customers will really love this revolutionary x4 PCI Express solution. Spartan-3 FPGA plus GL9714 solution can dramatically change people’s impression on the cost of 10 Gbps PCI Express implementation.” Mr. Wang continued, “Genesys Logic is committed to performance, compatibility, cost-effectiveness and ease-of-use on our PCI Express products. We view the success of our customers as our own success.” GL9714 has passed PCI-SIG’s compliance testing and is now on PCI-SIG’s latest PCI Express Integrators List. “We really appreciate Spinnaker’s promotion on low-cost PCI Express FPGA solutions. Genesys Logic will support Spinnaker - our most important partner in Japan without reservation.” added Mr. Wang.

PALTEK positions PALTEK PCI Express evaluation board as a great designing support tool for designing engineers and intend to promote it to wide range of applications.

### **About Xilinx**

Xilinx is the worldwide leader in complete programmable logic solutions. The company was founded in 1984 and has its headquarters in San Jose, California. Xilinx K.K. was founded in 1989 and the firm proactively sells and supports FPGA and CPLD products along with development support systems. Detailed information is available at the Japanese language website (<http://www.xilinx.co.jp>)

### **About Genesys Logic**

Genesys Logic is a fabless IC design house known for its leadership in high-speed I/O technologies. Genesys Logic develops its own Physical Layer high-speed SERDES for USB1.1/2.0, PCI Express and Serial ATA products. They provide low-cost, high-performance chips that target applications including PC peripherals, embedded systems and consumer electronics. More information is available at the Japanese website <http://www.genesyslogic.com/jindex01.asp> Spinnaker System is Genesys Logic’s distributor in Japan and for further sales inquiry, please contact Spinnaker System (Shin-Yokohama, Yokohama City, Phone: 045-478-3801)

### **About PALTEK CORPORATION:**

PALTEK is strong in providing solutions with Programmable Logic Devices (PLDs), Application Specific Standard Products (ASSPs) and other Networking related silicon. Based on sound business performance, PALTEK made its Initial Public Offering on JASDAQ in 1998. Rooting from its corporate philosophy of “kyo-sei” (cooperative growth), the company is continuously making efforts to contribute to the development of the ubiquitous society by providing the best available hardware and software solutions.

For further information, see <http://www.paltek.co.jp>

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