



Tadahito Takahashi, President and CEO  
2-3-12Shin-Yokohama, Kouhoku-ku,  
Yokohama 222-0033 Japan

## **PALTEK's PCI Express Board passed PCI-SIG's Compliance Test and made it on the Integrator List**

PALTEK CORPORATION (JASDAQ: 7587), a leading silicon and electronic solutions supplier in Japan, announced that its evaluation board for low cost x4 PCI Express (one lane is 2.5Gbps) solution has successfully completed the rigorous compliance test at PCI-SIG (PCI Express Special Interest Group), and now it is listed on the Integrators List. Thus, PALTEK became the first distributor company in the world to make the list. Both the evaluation board and add-in cards have passed the compliance test and are now listed.

(The Integrator List can be accessed from the following URL.

[http://www.pcisig.com/developers/compliance\\_program/integrators\\_list/pcie/pcie](http://www.pcisig.com/developers/compliance_program/integrators_list/pcie/pcie))

This PCI Express evaluation board, which is now certified as the PCI Express Rev.1.1 compliance, comprises of Xilinx's Spartan<sup>TM</sup>-3 FPGA, in which PCI Express IP core by Northwest Logic Inc, Oregon, the USA, is mounted, and the PHY device, GL9714, by Taiwan's Genesys Logic. With these components, this evaluation board can deliver fully complied and low-cost PCI Express solution, which enable shorter design cycle for the design engineers.

Mr. Osamu Sawada, Sr. marketing manager of Xilinx KK, said, "PALTEK's engineering team made it possible to deliver low-cost and broad transmission enabled PCI Express solution that customers ask for by using Xilinx's low cost Spartan<sup>TM</sup>-3 FPGA. Now that the YAMATO board is listed on the Integrator's List, the users will feel confident and comfortable choosing PALTEK's solution, and Xilinx can expect more demand from it."

"PALTEK has done it again!" said Jerry Chen, Architect and Director of Marketing at Genesys Logic America. "Their superb engineering work gives customers a vehicle to success for the low cost PCI Express applications." He added, "The listing of YAMATO board gives customers confidence and it will shorten customers' design cycle. PALTEK is a partner we trust."

### **About PALTEK PCI Express Evaluation Board:**

PALTEK PCI Express evaluation board (Product ID: PTKB-100002 YAMATO) comprises of following components and realized x4 PCI Express (1 lane: 2.5Gbps) at low cost.

- Spartan<sup>TM</sup>-3 FPGA by Xilinx
- GigaCourier<sup>TM</sup> GL9714, which is 100% PCI Express complied 4 lane 10Gb/s PHY device by Genesys Logic

More information is available at the Japanese language website

<http://www.paltek.co.jp/tech/pciex/palboard/index.htm>

### **About Xilinx**

Xilinx is the worldwide leader in complete programmable logic solutions. The company was founded in 1984 and has its headquarters in San Jose, California. Xilinx K.K. was founded in 1989 and the firm proactively sells and supports FPGA and CPLD products along with development support systems. Detailed information is available at the Japanese language website

<http://www.xilinx.co.jp>

---

### **About Northwest Logic**

Northwest Logic, founded in 1995 and located in Beaverton, Oregon, provides high-performance, easy-to-use IP Cores for ASICs and FPGAs.

Northwest Logic's PCI Express IP Cores are available in x1, x4 and x8 lane configurations. The cores are specifically designed for ease-of-use including built-in DMA engine, simple user interface and a complete status port. The DMA Engine provides very high throughput, on-demand DMA operation. The cores are provided with a comprehensive PCI Express Verification Suite. Northwest Logic also provides a fully certified PCI Express solution including boards, drivers and demonstration application.

For further information, see <http://www.nwlogic.com>

### **About Genesys Logic**

Genesys Logic is a fabless IC design house known for its leadership in high-speed I/O technologies. Genesys Logic develops its own Physical Layer high-speed SERDES for USB1.1/2.0, PCI Express and Serial ATA products. They provide low-cost, high-performance chips that target applications including PC peripherals, embedded systems and consumer electronics. More information is available at the Japanese website <http://www.genesyslogic.com/jindex01.asp>

Spinnaker System is Genesys Logic's distributor in Japan and for further sales inquiry, please contact Spinnaker System (Shin-Yokohama, Yokohama City, Phone: 045-478-3801)

### **About PALTEK CORPORATION:**

PALTEK is strong in providing solutions with Programmable Logic Devices (PLDs), Application Specific Standard Products (ASSPs) and other Networking related silicon. Based on sound business performance, PALTEK made its Initial Public Offering on JASDAQ in 1998. Rooting from its corporate philosophy of "kyo-sei" (cooperative growth), the company is continuously making efforts to contribute to the development of the ubiquitous society by providing the best available hardware and software solutions.

For further information, see <http://www.paltek.co.jp>

All trademarks and service marks are the property of their respective owners.

Questions concerning this Press Release:

PALTEK CORPORATION	Ikuko Tokunaga, Public Relations
Phone	81+45-477-2016
FAX	81+45-477-2012
E-mail address	<a href="mailto:pr@paltek.co.jp">pr@paltek.co.jp</a>